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Digital Design: An Embedded Systems Approach Using Verilog

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provides a foundation in digital design for students in computer engineering, electrical engineering and computer science courses. It takes an up-to-date and modern approach of presenting digital logic design as an activity in a larger systems

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design context. Rather than focus on aspects of digital design that have little relevance in a realistic design context, this book concentrates on modern and evolving knowledge and design skills. Hardware description language (HDL)-based design and

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HDL (hardware description language) usage at the abstract behavioural level and register transfer level, as well as for low-level verification and verification environments Includes worked examples throughout to enhance the reader's understanding and

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Companion Web site includes links to tools for FPGA design from Synplicity, Mentor Graphics, and Xilinx, Verilog source code for all the examples in the book, lecture slides, laboratory projects, and solutions to exercises

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In Digital Design Media, Second Edition, architects and related design professionals will find a complete conceptual guide to the multidimensional world of computer-aided design. In contrast to the many books that

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future—as a whole. Clear structure, numerous historical references, and hundreds of illustrations make this framework both accessible to the nontechnical professional and broadening for the experienced computer-aided designer. The

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